



News Release

FOR IMMEDIATE RELEASE

Elpida Honored With Technical Recognition Award from JEDEC Recognition Received for Our Important Long-Standing Contributions to Module Standardization

Elpida Memory is pleased to announce that Toshio Sugano, Technology and Development Office NPD Gr. Professional, has been awarded a Technical Recognition Award by the JEDEC Solid State Technology Association for his outstanding technical contributions to technology standardization.

The award recognizes Mr. Sugano's long-standing accomplishments and Elpida's contributions in standardizing DRAM modules, such as EDO DRAM and all the way to DDR2 SDRAM. The honor granted by JEDEC, the leading organization for developing solid-state standards, will help to elevate Elpida's status in module technology in the semiconductor industry and will inspire us to even greater achievements.

The list of Elpida's primary DRAM standardization successes is as follows:

- Establishing mechanical specifications for module and module connectors
- Introducing system simulation for the Dual Inline Memory Module (DIMM) standardization process
- Creation of module board design, component placement and other DIMM standard design rules

Another reason for the award is in recognition of the creation by Mr. Sugano and Elpida of JEITA (formerly, EIAJ), an ad hoc module group that has substantially benefited the Japanese domestic semiconductor industry through its energetic work on module standardization.

Elpida wishes to express its deepest appreciation for the honor bestowed by JEDEC and for the approval and support of our work shown by the semiconductor industry. The honor will encourage Elpida to continue to play a leading role in semiconductor technology development.

About JEDEC

JEDEC is the leading developer of standards for the solid-state industry. Almost 2,400 participants, representing some 270 companies, work together in 50 JEDEC committees to meet the technology needs of every segment of the industry, individual manufacturers and consumers. The publications and standards created by JEDEC are recognized around the world. All JEDEC standards are available online free of charge.

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For more information, please visit the JEDEC website at <http://www.jedec.org>.

About Elpida Memory, Inc.

Elpida Memory, Inc. is a manufacturer of Dynamic Random Access Memory (DRAM) silicon chips with headquarters based in Tokyo, Japan, and sales and marketing operations located in Japan, North America, Europe and Asia. Elpida's state-of-the-art semiconductor wafer manufacturing facilities are located in Hiroshima, Japan. Elpida offers a broad range of leading-edge DRAM products for high-end servers, mobile phones, digital television sets and digital cameras as well as personal computers. Elpida had sales of ¥207.0 billion during the fiscal year ending March 31, 2005. For more information, visit www.elpida.com.

The information contained within this news release, is current as of the date of release. Please note that the information herein may be revised later without prior notice.

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