



News Release

FOR IMMEDIATE RELEASE

Elpida Memory's 512 Megabit XDR™ DRAM Provides High Bandwidth of 8 Gigabytes Per Second for Digital Consumer Electronics

Production Using Elpida's 90 nm Process Technology Improves Yield and Performance

TOKYO, JAPAN, March 22, 2006 –Elpida Memory, Inc. (Elpida), Japan's leading global supplier of Dynamic Random Access Memory (DRAM), today announced the availability of 512 Megabit XDR™ DRAM devices in sample quantities. The new devices operate at 4.0 GHz data rate, providing an industry-leading data transfer rate of 8.0 Gigabytes per second (GB/s) within a single device for digital consumer electronics applications such as high definition televisions (HDTV), gaming consoles and home entertainment server systems that require high bandwidth to support 3-D graphics, superb digital imaging and advanced multimedia. XDR DRAM is based on the XDR memory interface architecture developed by Rambus®, Inc.

"The XDR architecture offers specific advantages for digital consumer applications, including extraordinarily high bandwidth per pin," said Yoshitaka Kinoshita, executive officer for the Digital Consumer Division of Elpida Memory, Inc. "Digital consumer applications represent the fastest growing segment of the market for Elpida Memory, and now that we are producing these devices on our 90 nm process, we expect to produce higher yield in response to the great demand."

"We are proud to partner with Elpida as they advance the performance of XDR DRAM, the world's fastest memory, on their state-of-the-art 90 nm process technology," said Laura Stark, senior vice president of Platform Solutions at Rambus "With the growing performance

demands of consumer electronic devices such as HDTV, Elpida's 4.0 GHz XDR DRAM offers an ideal solution."

Elpida 512 Megabit XDR DRAM – Technical Details

Elpida's 512 Megabit XDR DRAM (Part number: EDX5116ACSE) devices are organized as 4M words x 16-bits x 8 banks and with 4.0 GHz operation and 8.0 Gigabytes per second (GB/s) data transfer rate, more than 4 times the peak bandwidth of industry-standard DDR2 memory devices. They are manufactured using Elpida's 90 nm process technology and are available in 104-ball FBGA packages.

To support both high speed and robust data transfer, the devices utilize advanced Rambus-specific features such as Differential Rambus Signal Level (DRSL) interface, which minimizes the signal swing and noise, and Octal Data Rate (ODR) which transfers 8 bits per clock cycle to achieve 4.0 GHz operation even with the commonly used 400 MHz clock. The 512 Megabit XDR devices also feature programmable on-chip termination, adaptive impedance matching, dynamic request scheduling and zero overhead refresh.

Availability

Elpida's 512 Megabit XDR DRAM devices (Part number: EDX5116ACSE) are currently sampling to customers. Volume production is expected to begin based on market demand.

About Elpida Memory, Inc.

Elpida Memory, Inc. is a manufacturer of Dynamic Random Access Memory (DRAM) silicon chips with headquarters based in Tokyo, Japan, and sales and marketing operations located in Japan, North America, Europe and Asia. Elpida's state-of-the-art semiconductor wafer manufacturing facilities are located in Hiroshima, Japan. Elpida offers a broad range of leading-edge DRAM products for high-end servers, mobile phones, digital television sets and digital cameras as well as personal computers. Elpida had sales of ¥207.0 billion during the fiscal year ending March 31, 2005. For more information, visit www.elpida.com.

The information contained within this news release, is current as of the date of release. Please note that the information herein may be revised later without prior notice.

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Elpida Press Contacts:

<i>Asia/Japan</i> Tomoko Kobayashi Elpida Memory, Inc. (Japan) Tel: +81-3-3281-1648 E-mail: press@elpida.com	<i>North America</i> Tammy Lee Lee Communications – PR for Elpida Memory (USA) Inc. Tel: 1-207-563-5999 E-mail: tammy@leecommunications.com	<i>Europe</i> Peter Westerdorf Elpida Memory (Europe) GmbH. (Germany) Tel: 0049 (0)211 23945120 E-mail: peter.westerdorf@elpida.com
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