



News Release

FOR IMMEDIATE RELEASE

Elpida Offers Industry's First DDR2 SDRAM with x32-bit I/O Configuration

One-Chip Solution Simplifies System Board Layouts

TOKYO, JAPAN, May 15, 2008 – Elpida Memory, Inc. (Elpida), Japan's leading global supplier of Dynamic Random Access Memory (DRAM), today announced the availability of the industry's first 512-megabit DDR2 SDRAM with x32-bit I/O configuration. Elpida will begin sampling of this product by the end of this month.

In the past a DDR2 controller with x32-bit wide interface required two x16-bit I/O DRAMs. Now, Elpida offers a 512-megabit DDR2 SDRAM with x32-bit I/O configuration as a one-chip solution. The advantages of this x32-bit solution over a two 256-megabit DDR2 (with x16-bit configuration) solution are as follows:

- Approximately 25% memory area reduction on the board
- Fewer package balls (40 balls)
- Approximately 20% less power consumption
- A one-chip solution delivers low EMI and better signal quality

“Elpida's new x32-bit DDR2 product realizes a simpler board layout and it can support a four-layer board,” said Yoshitaka Kinoshita, executive officer for Elpida's Digital Consumer Division. “Elpida's one chip solution based on the new product is expected to reduce customer's total system costs.”

Elpida developed x32-bit I/O DDR2 products to meet the need for small-size consumer electronics that feature 1.8V low-voltage operations and low-power consumption. Because the new x32-bit product is specially designed for consumer devices it is well suited for use in digital consumer appliances.

Product Features:

- 512Mbit DDR2 SDRAM (x32-bit I/O configuration) using 70nm process technology
- Data transfer rate: 250Mbps to 1066Mbps (1GBytes/sec to 4.2GBytes/sec per DRAM device)
- ODT (On-Die-Termination): 50/75/150/225ohm
Enhanced 225ohm ODT reduces power consumption.
- Driver strength: normal/weak/quarter
Reduces overshoot, undershoot noises

Target Market:

Digital TVs, set-top boxes (STB), personal navigation devices (PND), printers, digital still cameras, digital video recorders, projectors and more.

Mass production of the new x32-bit DDR2 SDRAM is scheduled to begin in September 2008. Because Elpida has a proven track record of device production and support for early product launches in the digital consumer market, more than 30 companies in Japan and elsewhere in Asia, North America and Europe have welcomed the product. As a result, business discussions are in progress.

Elpida will continue to develop new products as it works toward that goal.

Product specifications for the new x32-bit SDRAM are attached.

About Elpida

Elpida Memory, Inc. (TSE 6665) is a leading manufacturer of Dynamic Random Access Memory (DRAM) integrated circuits. The company's design, manufacturing and sales operations are backed by world class technology expertise. Its 300mm manufacturing facilities, Hiroshima Plant and a Taiwan-based joint venture Rexchip Electronics, utilize the most advanced manufacturing technologies available. Elpida's advanced portfolio features such characteristics as high-density, high-speed, low power and small packaging profiles. The company provides DRAM solutions across a wide range of applications, including high-end servers, mobile phone and digital consumer electronics. More information can be found at <http://www.elpida.com>.

Information in this news release is current as of the timing of the release, but may be revised later without notice.

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Product specifications for the new x32-bit DDR2 SDRAM

	512-megabit DDR2 SDRAM with x32-bit I/O	<Reference> 256-megabit DDR2 SDRAM with 16-bit I/O
Part Number	EDE5132AABG	EDE2516AEBG
Data Transfer Rate	DDR2-1066 (7-7-7) DDR2-800 (5-5-5) DDR2-667 (5-5-5)	— DDR2-800 (5-5-5) DDR2-667 (5-5-5)
ODT	50/75/150/225 ohm	50/75/150 ohm
Driver Strength	Normal/Weak/Quarter	Normal/Weak
Design Process	70nm	70nm
Power Supply	1.8V+/-0.1V	1.8V+/-0.1V
Package	128-ball FBGA (13.5 x 10.5 x 1.2 mm)	84-ball FBGA (12.5 x 8.0 x 1.2 mm)